

Docket No.: 243647US2S

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

RE: Application Serial No.: 10/680,101

Applicants: Kanna TOMIYE, et al.

Filing Date: October 8, 2003

For: SEMICONDUCTOR DEVICE AND

SEMICONDUCTOR DEVICE MANUFACTURING

METHOD

Group Art Unit: 2812

Examiner: Lindsay Jr, Walter Lee

SIR:

Attached hereto for filing are the following papers:

ELECTION

Our check in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,

MAIER/& NEUSTADT, P.C.

Eckhard H. Kuesters

Registration No. 28,870

Customer Number

(703) 413-3000 (phone) (703) 413-2220 (fax)

> 1940 DUKE STREET ALEXANDRIA, VIRGINIA 22314 U.S.A. TELEPHONE: 703-413-3000 FACSIMILE: 703-413-2220 WWW.OBLON.COM

OBLON

SPIVAK

McClelland

MAIER

NEUSTADT

P.C.

ATTORNEYS AT LAW

ECKHARD H. KUESTERS (703) 413-3000 EKUESTERS@OBLON.COM



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

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KANNA TOMIYE ET AL

: EXAMINER: LINDSAY JR, WALTER L.

SERIAL NO: 10/680,101

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FILED: OCTOBER 8, 2003

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ELECTION

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SIR:

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In response to the election requirement dated February 4, 2005 Applicants elect Group I, Claims 1-29¹, drawn to a semiconductor device, classified in class 257, and subclass 1+ for further examination on the merits. Applicants reserve the right to file one or more divisional applications directed to the non-elected species.

Accordingly, examination on the merits of Claims 1-29 is believed to be in order and an early and favorable action is respectfully requested.

Respectfully submitted,

Customer Number

22850

Tel: (703) 413-3000 Fax: (703) 413 -2220 (OSMMN 08/03) MAIER & NEUSTADT, P.C.

OBLON, SPIVAK, McCLELLAND,

Eckhard H. Kuesters Registration No. 28,870 Attorney of Record

¹ Although the outstanding restriction identifies Claim 36 as belonging to Group I, such grouping of method Claim 36 with the Group I device claims appears to have been in error. Accordingly, device Claims 1-29 are elected.